

AMENDMENTS TO CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A burst transfer mechanism of network packets having MAC frames over a USB bus ~~is designed~~ in an Application Specific Integrated Circuit (ASIC) installed in a USB compliant networking device, which enables the networking device to proceed with the steps of:

when the networking device receives a Bulk I/O request packet, transmitting the Bulk I/O request packet to a USB driver installed therein;

assembling enabling the USB driver to assemble a plurality of Ethernet network packets having MAC frames together ~~as~~ append proprietary bytes to every Ethernet packet ~~being received to delineate the end of each Ethernet packet, and form~~ a super-size network packet;

transferring the super-size network packet into a plurality of USB packets having maximum packet size defined for the USB endpoint ~~while receiving a Bulk In/Out request packet; and~~

transmitting the USB packets out within a burst cycle of a Bulk In/Out transaction.

2. (Canceled)

3. (Currently Amended) The mechanism of claim 1, wherein the proprietary bytes ~~comprises~~ comprise the length and length bar (inversion of Length value) of the corresponding ~~network~~ Ethernet packets, wherein the length refers to the total quantity of bytes counting from the beginning of the destination address field to the end of data field, and is used to represent the ending position of each ~~MAC frame~~ network ~~Ethernet~~ packet being received and to delineate the boundary thereof.

4. (Currently Amended) The mechanism of claim ~~2~~1, wherein, when the USB driver grants the request of the Bulk I/O request packet after receiving the Bulk I/O request packet, the mechanism proceeds with the encapsulation process with respect to the super-size network packet, transferring the super-size network packet into a plurality of USB packets having maximum packet size defined for the USB endpoint along with a short packet and then, within ~~at~~the burst cycle of ~~the~~ Bulk In/Out transaction, transmitting the USB packets out.

5. (Currently Amended) The mechanism of claim 4, wherein, within ~~at~~the burst cycle of ~~the~~ Bulk In/Out transaction, the maximum quantity of the ~~network~~Ethernet packets having MAC frames ~~being comprised to be included~~ in a super-size network packet ~~can be~~is predefined in a Burst Credit according to the quantity of the ~~network~~Ethernet packets having MAC frames currently queuing in a buffer installed in the networking device.